Translation of amendment under Article 19(1)

8.(Original) A method of executing a program in a multiprocessor system, the method comprising, executing a call instruction in a main routine running by a first processor, delegating to a second processor the task of saving a return address for returning to the main routine upon completion of processing of a subroutine called by the call instruction.

9.(Original) The method of executing a program in a multiprocessor system according to claim 8, wherein:

if a stack area inside the first processor has a free space, the first processor saves the return address to the stack area by itself; and

if the stack area has no free space, the save of the return address is delegated to the second processor.

10.(Original) The method of executing a program in a multiprocessor system according to claim 8, wherein:

if the call instruction does not explicitly instruct to delegate the task of saving the return address to the second processor, the first processor saves the return address to a stack area built in itself; and

if the call instruction explicitly instructs to delegate the task of saving the return address to the second processor, the first processor delegates the task of saving the return address to the second processor.

11. (Amended) A method of executing a program in a

multiprocessor system, the method comprising, executing a call instruction or a jump instruction by a first processor, delegating a task of acquiring a call destination address or a jump destination address to a second processor if the number of bits of the call destination address or jump destination address exceeds the number of bits acquirable by the first processor.

12. (Amended) A method of executing a program in a multiprocessor system, the method comprising, executing a call instruction or a jump instruction by a first processor, delegating a task of acquiring a call destination address or jump destination address to a second processor if the call instruction or the jump instruction explicitly instructs to delegate the task of acquiring the call destination address or the jump destination address to the second processor.

13. (Canceled)